HINDUSTHAN COLLEGE OF ARTS & SCIENCE (AUTONOMOUS) COIMBATORE – 641028.

B.Sc. ELECTRONICS AND COMMUNICATION SYSTEMS SCHEME OF EXAMINATIONS – CBCS PATTERN

(For the students admitted from the Academic year 2017 - 2018 and onwards)

		~ ¥	Hrs.	Max. Marks		ts	
CODE No.	SUBJECT	LECTURE Hrs./ WEEK	EXAM. Dur. F	IE	EE	Total	Credit Points
First Semes	ster						
	Part - I			100	No.		
16LAT01/ 16LAH01/ 16LAF01/ 16LAM01	Tamil - I/Hindi - I/French - I /Malayalam - I	6	3	25	75	100	3
70.09	Part - II	APPLICATION OF THE PERSON OF T	100	1 -57		DEN:	
16ENG01	English - I	6	3	25	75	100	3
	Part - III						
17ELU01	Principles of Electronics	6	3	25	75	100	5
17ELU02	Allied: Mathematics-I (MAT)	6	3	25	75	100	5
17ELU05	Practical I: Principles of Electronics Lab	3	-			-	-
17ELU06	Practical II: Semiconductor Devices Lab	3	-	-	-		-
Second Sen	nester			I			
	Part - I	THE REAL PROPERTY.					
16LAT02/ 16LAH02/ 16LAF02/ 16LAM02	Tamil -II/Hindi -II/French -II /Malayalam -II	6	3	25	75	100	3
	Part - II			Dis.	10,0	9-42	
16ENG02	English - II	6	3	25	75	100	3
	Part - III					T COST	
17ELU03	Semiconductor Devices	5	3	25	75	100	5
17ELU04	Allied: Mathematics-II (MAT)	5	3	25	75	100	5
17ELU05	Practical I: Principles of Electronics Lab	3	3	40	60	100	4
17ELU06	Practical II: Semiconductor Devices Lab	3	3	40	60	100	4
1.0001101	Part - IV			1.00		100	
16GSU01 Third Seme	Value Education - Human Rights	2	- 1	100	-	100	2
Tillru Seine	Part - III	A STATE OF				3 N. Y.	
17ELU07	Principles of Communication System	5	3	25	75	100	4
17ELU08	Digital Electronics and its Applications	5	3	25	75	100	4
17ELU08		5	3		75	100	
17ELU10	Electronic Circuits Programming in C	4	3	25 25	75	100	4
							4
17ELU11	Practical III: C Programming Lab	3	3	40	60	100	2

17ELU15	Practical IV: Electronic Circuits Lab	3	-	-	-	-	-
17ELU16	Practical V: Analog and Digital IC Lab	3	-	7-		-	-
	Part - IV		Open pl-Sy		<u> </u>	NY 1	
16GSU02	Environmental Studies	2	-	100	-	100	2
Fourth Sen	nester						
	Part - III						
17ELU12	Microwave and Fiber Optic Communication	5	3	25	75	100	4
17ELU13	Integrated Circuits and Instrumentation	5.	3	25	75	100	4
17ELU14	PCB Design and Fabrication	5	3	25	75	100	4
17ELU15	Practical IV: Electronic Circuits Lab	4	3	40	60	100	4
17ELU16	Practical V: Analog and Digital IC Lab	4	3	40	60	100	4
17ELU17	Practical VI: Communication Systems Lab	5	3	40	60	100	4
	Part - IV		er i		On the		
16GSU03	Skill Based: Internet Security	2	-	100	-	100	2
	Part - V			4			
16GSU04	Extension Activity	-	-	100	2.=	100	2
Fifth Seme	ster .						
	Part – III		60.		100	13K	
17ELU18	Cellular Communication System	5	3	25	75	100	4
17ELU19	VLSI Design Tools	5	3	25	75	100	4
17ELU20	8051 Microcontroller	5	3	25	75	100	4
17ELU21	Practical VII: Circuit Simulation Lab	5	3	40	60	100	2
17ELU26	Practical VIII: Industrial Electronics Lab	3	-	-	1-	-	-
17ELU27	Practical IX: Embedded Systems Lab	3	-	-	-	-	-
17ELU22	Elective - I (a) Medical Electronics (b) Consumer Electronics OR	4	3	25	75	100	4
	Part - IV		-				
16GSU05	Non - Major Elective: General Awareness	-	-	100	-	100	2
	Part - V		-	233	1000	178	
16GSU06	Law of Ethics	-	-	100	-	100	2
Sixth Seme	ster					10.0	
	Part – III		_				
17ELU23	Satellite and Network Communication	5	3	25	75	100	4
17ELU24	Industrial and Power Electronics	5	3	25	75	100	4
17ELU25	Embedded Systems	5	3	25	75	100	4
17ELU26	Practical VIII: Industrial Electronics Lab	3	3	40	60	100	4
17ELU27	Practical IX: Embedded Systems Lab	3	3	40	60	100	4
17ELU28	Elective - II (a) Robotics and Automation OR (b) Automotive Electronics	5	3	25	75	100	4
17ELU29	Project Work	4	120	40	60	100	4
							14

REGULATIONS

• Components for Evaluation:

1. Internal Examination Marks (For Part III theory papers)

Components	Marks
Test –I & II (Best of Two)	10
Model Exam	10
Assignment	5
Total	25

QUESTION PAPER PATTERN FOR I.E TEST I and II

(2 HOURS TEST)

MAXIMUM: 50 Marks

SECTION - A (20 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(10 \times 2 = 20 \text{ marks})$

Short answers 10

SECTION - B (10 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(2 \times 5 = 10 \text{ marks})$

Either or Type

SECTION - C (20 Marks)

Answer any TWO Questions out of THREE questions

ALL Questions Carry EQUAL Marks

 $(2 \times 10 = 20 \text{ marks})$

QUESTION PAPER PATTERN FOR IE Model Examination

(3 HOURS TEST)

MAXIMUM: 75 Marks

SECTION - A (20 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(10 \times 2 = 20 \text{ marks})$

TWO questions from each unit

SECTION - B (25 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(5 \times 5 = 25 \text{ marks})$

Either or Type.

ONE question from each unit with internal choice

SECTION - C (30 Marks)

Answer any **THREE** Questions out of **FIVE** questions **ALL** Questions Carry **EQUAL** Marks ONE question from each unit

 $(3 \times 10 = 30 \text{ marks})$

2 a) Components for Practical I.E.

20
20
40

2 b) Components for Practical E.E.

Marks
50
5
5
60

3. Institutional/Industrial Training, Mini Project and Major Project Work

Institutional /Industr	rial Training	Mini Project	Majo	r Project Wor	<u>-k</u>
Components	Marks	Marks	Compon	ents	Marks
I.E Work Diary	25		I. E a) Attendance	10 Marks	
Report	50	50	b) Review /		40
Viva –voce	25	50	Work Diary*1	30 Marks	
Examination					
Total	100	100	E.E * ²		
			a) Final Report	40 Marks	
	-		b) Viva-voce	20 Marks	60
				Total	100

^{*&}lt;sup>T</sup> Review is for Individual Project and Work Diary is for Group Projects (Group consisting of minimum 3 and maximum 5)

^{*&}lt;sup>2</sup>Evaluation of report and conduct of viva voce will be done jointly by Internal and External Examiners

4. Components for Value Education (Part IV):

S. No.	Components	Marks
a)	Attendance	30 marks
	96% and above - 30 marks	
	91% to 95% - 25 marks	
	86% to 90% - 20 marks	
	76% to 85% - 10 marks	
b)	Participation in group activity	30 marks
c)	Assignment (2 x 10)	20 marks
d)	Test	20 marks
	(1 hr for 20 marks)	
	2 out of three questions, 10 marks each	
	Total	100 marks

On completion of the above components students will be remarked as follows:

Range of marks	Equivalent remarks
80 and above	Exemplary
70 - 79	Very good
60 - 69	Good
50 - 59	Fair
40 - 49	Satisfactory
Below 39	Not Satisfactory = Not completed

- The passing minimum for this paper is 40%
- In case, the candidate fails to secure 40% passing minimum, he / she may have to reappear for the same in the subsequent semesters.

5. <u>Guidelines for Environmental Studies</u> (Part IV)

- The paper Environmental Studies is to be treated as 100% IE course which is offered in III Semester for II year UG students.
- The classes will be handled for two hours per week till the end of the Semester. At least one field trip should be arranged.
- Total Marks for the subject = 100

Components	Marks
Two Tests (2 x 30)	60
Field visit and report (10 + 10)	20
Two assignments (2 x 10)	20
Total	100

The question paper pattern is as follows:

Test I – 2 hours [3 out of 5 essay type questions] $3 \times 10 = 30$ Marks

Test II – 2 hours [3 out of 5 essay type questions] $3 \times 10 = 30$ Marks

Total 60 Marks

• The passing minimum for this paper is 40%

• In case, the candidate fails to secure 40% passing minimum, he / she may have to reappear for the same in the subsequent semesters.

6. Guidelines for Skill based subject - Internet Security (Part IV)

Components	Marks
Гwo Tests (2 x 40)	80
Γwo assignments (2 x 10)	20
Total	100

The question paper pattern is as follows:

a) Test I - 2 hours [4 out of 7 essay type questions]

 $4 \times 10 = 40 \text{Marks}$

b) Test II -2 hours [4 out of 7 essay type questions]

 $4 \times 10 = 40 \text{ Marks}$

Total 80 Marks

• The passing minimum for this paper is 40%

• In case, the candidate fails to secure 40% passing minimum, he / she may have to reappear for the same in the subsequent semesters

7. Guidelines for General Awareness (Part IV)

Marks
100

The question paper pattern is as follows:

Test I - 2 hours [50multiple choice questions]

 $50 \times 1 = 50 Marks$

Test II – 2 hours [50 multiple choice questions]

 $50 \times 1 = 50 \text{ Marks}$

Total 100 Marks

• The passing minimum for this paper is 40%

• In case, the candidate fails to secure 40% passing minimum, he / she may have to reappear for the same in the subsequent semesters

8. Guidelines for Law of Ethics (Part V)

Components	Marks
Two Tests (2 x 50)	100

The question paper pattern is as follows:

- c) Test I 2 hours [5 out of 8 essay type questions] $5 \times 10 = 50$ Marks
- d) Test II 2 hours [5 out of 8 essay type questions] $5 \times 10 = 50 \text{ Marks}$

Total 100 Marks

- The passing minimum for this paper is 40%
- In case, the candidate fails to secure 40% passing minimum, he / she may have to reappear for the same in the subsequent semesters

9. Guidelines for Extension Activity (Part V)

- Atleast two activities should be conducted within this semester (IV) consisting of two days each.
- The activities may be Educating Rural Children, Unemployed Graduates, Self Help Group etc.

The marks may be awarded as follows

No of Activities	Marks
2 x 50	100
(Each Activity for two days)	

10. QUESTION PAPER PATTERN FOR EE (Part III Theory Papers)

(3 HOURS TEST)

MAXIMUM: 75 Marks

SECTION - A (20 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(10 \times 2 = 20 \text{ marks})$

TWO questions from each unit

SECTION - B (25 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks

 $(5 \times 5 = 25 \text{ marks})$

Either or Type.

ONE question from each unit with internal choice

SECTION - C (30 Marks)

Answer any THREE Questions out of FIVE questions

ALL Questions Carry EQUAL Marks

 $(3 \times 10 = 30 \text{ marks})$

ONE question from each unit

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Code No	Subject	Semester No
17ELU01	PRINCIPLES OF ELECTRONICS	I
Objective:	To enable the students to learn, the passive components and their ap Electronic Circuits.	plications in
Unit No	Topics	Hours
Unit I	Introduction to Electricity Negative and positive polarities – Structure of atom – Electric Charge – Voltage – Current – Direct Current (DC) – Alternating Current (AC) – Frequency – Period – Wavelength – Phase angle – Sources of electricity.	15
Unit II	Fundamentals of Electronic Components Resistor: Ohms Law – Color coding – Types: Fixed and Variable – Rheostats and Potentiometers – Power rating – Resistors in serial and parallel. Capacitor: Principles of capacitance – Types: Electrolytic capacitors – Capacitor coding – Capacitor in series and parallel. Inductor: Principles of inductance – Types – Inductor in serial and parallel.	15
Unit III	Basic Laws of Electronic Circuits Power dissipation in resistance – Voltage and Current Dividers – Kirchoff's Voltage Law (KVL) – Kirchoff's Current Law (KCL) – Method of branch Currents – Node Voltage Analysis – Method of mesh currents – Concept of voltage source and current source – Voltage source in series and current source in parallel.	15
Unit IV	Network Theorems Superposition Theorem – Thevenin's Theorem – Thevenizing a circuit with two voltage source – Thevenizing a bridge circuit – Nortons theorem – Thevenin-Norton Conversion – Conversion of Voltage and Current Sources – Millman's Theorem – Maximum Power Transfer Theorem – T or Y and π or Δ Connections.	15
Unit V	AC Circuits Alternating Current: Resistance circuit – Capacitance circuit (X_C) – Inductance circuit (X_L) – X_L and X_C in series and parallel – Analysis of resonance circuits – Series resonance – Parallel resonance. RC Filters: RC low pass filter – RC high pass filter – RC band pass filter – RC band stop filter.	15

1. Bernard Grob and Mitchel E Schultz "Basic Electronics", Tata McGraw Hill, 9th Edition, 2005.

Reference Books:

- 1. S.Salivahanan, N.Suresh Kumar & A.Vallavaraj, "Electronic Devices and Circuits", Tata McGraw-Hill, 1998.
- 2. B.V.Narayana Rao, "Principles of Electronics", Wiley Eastern Limited, 1992.
- 3. B.L. Theraja, "Basic Electronics-Solid State Devices", S. Chand, 2000.
- 4. Malvino, Albert Paul, "Electronic Principles", Tata McGraw Hill, Sixth Edition, 2004.
- 5. V.K. Mehta, Rohit Mehta, "Principles of Electronics", S. Chand and Company Ltd., 2005.

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Code No.	Subject	Semester No.
17ELU03	SEMICONDUCTOR DEVICES	II
Objective:	To equip the students to learn the construction, working and character semiconductor devices.	stics of variou
Unit No	Topics	Hours
Unit I	Atomic Structure and Semiconductor Physics Bohr's atomic model – Energy levels – Energy bands – Energy bands solids – Classification of solids and energy bands – Semiconductor Bonds in semiconductors – Energy band description of semiconductors Effect of temperature on semiconductor – Hole current – Intrins semiconductor – Extrinsic semiconductor – n-type and p-ty semiconductor – Majority and minority carriers – PN junction – Forwal and reverse biased PN junction.	- - sic pe
Unit II	Semiconductor Diodes Zener diode – Zener diode as a voltage regulator – Tunneling effect at Tunnel diode – Varactor diode – PIN diode – Schottky Diode – Strecovery diode – Thermistor – Gunn diode – LED – Photo diode.	
Unit III	Transistors Introduction – Transistor as an Amplifier – CB, CE and C Configurations – Comparison of transistor configurations – Transist load line analysis – Operating point – Cut off and saturation points Power ratings – Transistor biasing and its types.	or 12
Unit IV	Field Effect Transistors Junction Field Effect Transistor – Operation – Transfer characteristics Comparison of FET and BJT – MOSFET – Types: Depletion Enhancement –Drain and transfer characteristics – MOSFET as resistor– Advantage of N-Channel MOSFET over P-Channel.	_ 12
Unit V	Thyristors Overview - Construction and Working: Silicon Controll Rectifier(SCR) - TRIAC - DIAC - Unijunction Transistor (UJT Operation - UJT relaxation oscillator - Silicon Controlled Switch (SCS) Silicon Unilateral Switch (SUS) - Silicon Bilateral Switch (SBS).	T): 12

- 1. V.K. Mehta, "Principles of Electronics", S.Chand, 1997. (Unit-I, II & III).
- 2. R.S. Sedha, "A Text Book of Applied Electronics", S.Chand, 3rd Revised Edition, 2008. (Unit-IV & V).

- 1. B.L. Theraja "Basic Electronics Solid State", S. Chand, 1998.
- 2. S. Salivahanan, N. Suresh Kumar & A. Vallavaraj, "Electronics Devices and Circuits", Tata McGraw Hill Publishing Company Limited, New Delhi, 8th edition.
- 3.S.M.Sze, "Semiconductor Devices: Physics and Technology", Wiley India (P.) Ltd, Second Edition, 2008.



Code No.	Subject	Semester No.
17ELU05	PRACTICAL I: PRINCIPLES OF ELECTRONICS LAB	II

(Any 18 Experiments)

- 1. Familiarization of components: Active & Passive identification (colour coding), specification and testing using Multimeter.
- 2. Measurement of Amplitude, Frequency & Phase difference using CRO.
- 3. Verification of Ohm's Law.
- 4. Voltage sources in series, parallel and series & parallel.
- 5. Resistors in series, parallel and series & parallel.
- 6. Capacitors and Inductors in series, parallel and series & parallel.
- 7. Verification of Kirchhoff's Law.
- 8. Verification of Voltage Divider Rule.
- 9. Verification of Current Divider Rule.
- 10. Verification of Superposition Theorem.
- 11. Verification of Thevenin's Theorem.
- 12. Verification of Norton's Theorem.
- 13. Verification of Millman's Theorem.
- 14. Verification of Maximum Power Transfer Theorem.
- 15. Series Resonance Circuit.
- 16. Parallel Resonance Circuit.
- 17. RC Low Pass Filter.
- 18. RC High Pass Filter
- 19. RC Band Pass Filter.
- 20. RC Band Rejection Filter.
- 21. Transient response of RC Circuit.
- 22. Transient response of RL Circuit.
- 23. Frequency response of R, L & C.
- 24. Wheatstone Bridge.
- 25. LCR Bridge.

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er No	Semester	Subject	Code No.
I	II	PRACTICAL II: SEMICONDUCTOR DEVICES LAB	17ELU06
	I	PRACTICAL II: SEMICONDUCTOR DEVICES LAB	17ELU06

(Any 18 Experiments)

- 1. Band Gap Energy of Silicon / Germanium Diode.
- 2. Temperature Co-efficient of Junction Diode.
- 3. Characteristics of PN Junction Diode.
- 4. Characteristics of Zener Diode.
- 5. Characteristics of Light Emitting Diode.
- 6. Common Emitter (CE) Characteristics of Transistor.
- 7. Common Base (CB) Characteristics of Transistor.
- 8. Common Collector (CC) Characteristics of Transistor.
- 9. Transistor Biasing Circuits.
- 10. Transistor as a Switch.
- 11. Characteristics of JFET.
- 12. Characteristics of MOSFET
- 13. Characteristics of SCR.
- 14. Characteristics of TRIAC.
- 15. Characteristics of DIAC.
- 16. Characteristics of UJT.
- 17. UJT Relaxation Oscillator.
- 18. Zener Diode as Voltage Regulator.
- 19. FET as Voltage Variable Resistor (VVR).
- 20. Measurement of stability factor of Fixed Bias.
- 21. Measurement of stability factor of Self Bias.
- 22. Characteristics of LDR.
- 23. Characteristics of Solar Cell.
- 24. Study of IR Transmitter & Receiver.
- 25. Study of Seven Segment Display.

Code No.	Subject	Semester No.
17ELU07	PRINCIPLES OF COMMUNICATION SYSTEM	III
Objective:	To equip and enable the students to learn the basic principles used in Electron	
	Communication Systems.	ľ
Unit No	Topics	Hours
Unit I	Radio Wave Propagation and Antennas Electromagnetic Radiation – Ground Wave, Sky Wave and Space War Propagation – Radiation Mechanism – Elementary Doublet – Resona and Non resonant Antenna – Terms and Definitions of Antennas – Dipo Array – Folded Dipole – Parabolic Reflector – Horn Antenna – Le Antenna – Helical Antenna – Discone Antenna – Log Periodic and Log Antennas.	nt le ns 12
Unit II	Amplitude Modulation Introduction – Need of Modulation – External Noise – Internal Noise Noise Figure – Noise Temperature. Amplitude Modulation: Theory Frequency Spectrum – Representation – Bandwidth and Power Relation AM Generation: Balanced Modulator – Filer Method, Phase-Shift Methol and Third Method of AM generation – Independent Sideband System Vestigial Sideband Transmission.	on. 12
Unit III	Frequency and Phase Modulation Frequency Modulation: Theory – Mathematical Representation Frequency Spectrum – Bandwidth and Power Relation – Pha Modulation – Pre-emphasis and De-Emphasis Comparison of Wide a narrow Band FM. FM Generation: Direct Method – Stabilized Reactan Method – Armstrong Method.	se 12
Unit IV	Radio Receivers Tuned Radio Frequency (TRF) Receivers – Super heterodyne Receiver AM Receivers: RF Section – Frequency Changing and tracking – Amplifiers – Detectors and AGC – Basic Block diagram of AM receiver FM Receivers: Basic Block Diagram – Comparison of FM receiver w AM receiver – Amplitude Limiting – FM Demodulators – Slo Detection – Balanced Slope Detection – Phase Discriminator – Rad Detector – SSB Receiver – ISB Receiver.	IF er. 12 ith pe
Unit V	Pulse and Digital Modulation Pulse Amplitude Modulation – Pulse Width Modulation – Pulse Positi Modulation – Pulse Code Modulation – Differential PCM – De Modulation – Quantization Noise – ASK – FSK – PSK – BPSK – QA and QPSK.	lta 12

- 1. George Kennedy, "Electronic Communication Systems", Tata McGraw- Hill. (Unit I, II, III and IV)
- 2. Harold Kolimbiris, "Digital Communication Systems with Satellite and Fiber Optics Applications", Pearson Education, Third Indian Reprint, 2004. (Unit-V)

Reference Books:

- 1. Herbert Taub, Donald L Scilling and Goutam Saha, "Principles of Communication Systems", Tata McGraw Hill, Third Edition, 2008.
- 2. Dennis Roddy and John Coolen, "Electronic Communication", Pearson Education, Fourth Edition, 2008.
- 3. Anokh singh & A.K. Chhabra, "Principles of Communication Engineering", S.Chand & Company Ltd, Sixth Edition, 2007.

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Code No.	Subject	Semester No.
17ELU08	DIGITAL ELECTRONICS AND ITS APPLICATIONS	III
Objective:	To equip the students to learn with detailed knowledge in number system logic gates, comparators, flip flops, DACs and ADCs.	ns, digital IC's,
Unit No	Topics	Hours
Unit I	Number System and Codes Binary Number System – Binary to Decimal Conversion – Decimal Binary Conversion – Octal Numbers – Hexadecimal numbers – ASC codes – Excess three – Gray code – Error Detection and Correction.	
Unit II	Boolean Algebra and Logic Gates Basic Logic Gates – Universal Logic Gates – AND-OR-Invert Gates Positive and Negative Logic – Boolean Laws and theorems – SOP Karnaugh Map Simplifications – Don't care Conditions - POS – PO Simplification.	- 12
Unit III	Arithmetic Circuits Binary Addition - Binary Subtraction - Unsigned Binary Numbers- Sig magnitude Numbers - 2's Complement Representation - 2's Compleme Arithmetic - Adder-Subtractor - Arithmetic Logic Unit - Bina Multiplication and Division - Multiplexers - De-multiplexers - Decode Encoder	nt 12
Unit IV	Sequential Logic Circuits Flip Flops: RS, Clocked RS, D, JK, JK Master Slave and T Flip Flop Counters: Asynchronous counter – Synchronous counter – Up/Dov counter – Modulus counters – Decade counter. Shift Registers: Seri In/Serial Out – Serial In/Parallel Out – Parallel In/Serial Out – Parallel In/Parallel Out.	vn al 12
Unit V	D/A and A/D Converters Digital to Analog converters: Weighted Resistor Method – R-2R Ladd Method – Accuracy and Resolution of DAC. Analog to Digit converters: Simultaneous converter – Counter type converter Continuous type converter –Successive approximation type converter Dual slope converter – Accuracy and resolution of ADC.	al 12

1. Donald P. Leach, Albert Paul Malvino & Goutam Saha, "Digital Principles and Applications", Tata McGrawHill, 7th Edition, 2011.

- Thomas L. Floyd & R.P.Jain, "Digital Fundamentals", Pearson Education, 8th Edition, 2005.
 M.Morris Mano, "Digital Logic and Computer Design", PHI, 2005.
 M.Morris Mano, "Digital Design", PHI, 2005.



Code No.	Subject	Semester No.
17ELU09	ELECTRONIC CIRCUITS	III
Objective:	This subject describes the working of rectifiers, filters, voltage regulated	
	types of amplifiers & oscillators and to enable the students to become circuit designer.	e an electronic
Unit No	Topics	Hours
CHIT NO	Topics	110410
Unit I	Rectifiers, Filters and Voltage Regulators Half wave, Full wave and bridge rectifiers – PIV-DC output voltage Ripple Factor – Efficiency – Transformer utilization factor – Filter Inductor Filter – Capacitor Filter – LC Filter – π-Filter – Voltage Regulation – Zener Diode Shunt Regulator – Transistor Shunt and Seri Regulator – DC Power Supply.	rs: 12 ge
Unit II	Single and Multistage Transistor Amplifiers Transistor as an Amplifier – Common Emitter, Common Base an Common Collector Amplifiers – Input Resistance, Output Resistance Current Gain, Voltage Gain and Power Gain of CE, CB & CC Amplifier Gain of Multistage Amplifiers – RC Coupled Amplifier – Impedant Coupled Amplifier – Transformer Coupled Amplifier – Direct Coupled Amplifier – Frequency response, Advantage, disadvantage and Applications – Darlington pair Amplifier.	re, rs, ce ed
Unit III	Power Amplifiers Performance parameter and AC load line – Classifications – Class power Amplifier – Class B Power Amplifier – Class B Push-Pu Amplifier – Crossover Distortion – Class C Amplifiers – Characteristic and overall efficiency of Class A, Class B and Class C Power Amplifier	ull 12 cs
Unit IV	Feedback Amplifiers Principle of Feedback amplifiers – Types – Advantages and disadvantag negative feedback – Gain stability – Bandwidth– Distortion – Noise Voltage Series, Voltage Shunt, Current Series and Current Shu Feedbacks – Comparison of feedback connections – Negative Feedback transistor amplifier – CE amplifier without emitter bypass capacitor Emitter Follower – Negative feedback in multistage amplifiers.	es nnt in 12
Unit V	Oscillators and Multivibrators Barkhausen Criterion – Hartley Oscillator – Colpitt's Oscillator – Cla Oscillator – Quartz crystal – RC Phase Shift – Wien Bridge Oscillators Astable, Monostable and Bistable Multivibrators – Schmitt Trigger a wave shaping circuits.	5 – 12

1. R.S. Sedha, "A Text Book of Applied Electronics", 3rd Revised Edition, S.Chand, 2008.

- 1. S.K. Sahdev, "Electronic Principles", Dhanpat Rai & Co (P) Ltd, 2nd Edition, 1998.
- 2. B.L. Theraja, "Basic Electronics Solid State", S. Chand Company Ltd., 2000.
- 3. S.Salivahanan, N.Suresh Kumar & A.Vallavaraj, "Electronic Devices and Circuits", Tata Mc Graw Hill, Second Edition, 2008.

Code No.	Subject	Semester No.
17ELU10	PROGRAMMING IN C	III
Objective:	On successful completion of this subject the students have the programm C Language.	ng ability in
Unit No	Topics	Hours
Unit I	Introduction Overview of C - Names - Data types and Qualifiers - Constant - variables - Assignment of variables - Variable Initialization. Operators Arithmetic, Assignment, Relational, Logical, Bitwise, Conditional Compound assignment, Unary and other operators - Order of precedence and associativity of operators.	: 9
Unit II	Program Control Constructs Conditional & multiple branching iteration – Jump constructs – Console input and output: Console I/O Functions – getch, putch, getchar, putchar gets puts, print, and scan functions – Formatted I/O . Functions Definition– prototype – recursion – simple programs.	, 9
Unit III	Arrays Initializations – Multidimensional arrays – character arrays. Pointers Declaration and initialization of pointer variables – Pointers and Functions – Pointers and arrays – Pointers and strings – Arrays of pointers and pointer to an array – Command line arguments – Dynamic memory allocation.	9
Unit IV	Structures & Unions Definition & initializing structure variables – Array of structures – Pointer to structures – Array of pointers structures – Passing structures to functions – bit fields – Union.	9
Unit V	Files File structure – Opening & closing of files – Character functions – Line I/O functions – Formatted I/O functions – Block I/O functions. Preprocessor directive: file inclusion – Macro substitution – Conditional compilation.	9

1. E.Balagurusamy, "Programming in ANSI C", Tata McGraw-Hill, 2004.

- 1. Yashavant P. Kanetkar, "Let us C", BPB Publications, 14th Edition, 2016.
- 2. Stephen G. Kochan, "Programming in C", Addison-Wesley, Fourth Edition, 2015.
- 3. Rajaraman.V, "Computer Programming in C", Prentice-Hall of India Private Limited, Sixteenth Printing, 2006.

Code No.	Subject	Semester No.
17ELU11	PRACTICAL III: C PROGRAMMING LAB	III

(Any 10 Experiments)

- 1. Find the sum, average, standard deviation for a given set of numbers.
- 2. Generate n prime numbers.
- 3. Generate Fibonacci series.
- 4. Find the greatest among the Three Numbers.
- 5. Check Whether the Given Number is Armstrong Number or Not.
- 6. Find the Sum of Given All Digits.
- 7. Find the Given Number is Prime or not.
- 8. Find the given number is odd or even.
- 9. Matrix Addition, Subtraction and Multiplication.
- 10. Sort the given set of numbers in ascending order.
- 11. Check whether the given string is a palindrome or not using Pointers.
- 12. Count the number of Vowels in the given sentence.
- 13. Find the factorial of a given number using recursive function.
- 14. Print the students Mark sheet assuming roll no, name, and marks in five subjects in a structure. Create an array of structures and print the mark sheet in the university pattern.
- 15. Write a function using pointers to add two matrices.

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Code No.	Subject	emester No.
17ELU12	MICROWAVE AND FIBER OPTIC COMMUNICATION	IV
Objective:	To learn the basics of Wave Guides, fundamentals of Electromagnetic Waves Microwaves, Concepts of Optical Fibers, the principles of Optical sensors and Applications.	& its
Unit No	Topics	Hours
Unit I	Microwave and Electromagnetics Microwave region and band designations - Advantages & Applications of Microwave - Electro Magnetic wave principles - Maxwell's Equations: Amperes Law - Faraday's Law - Gauss's Law - Wave Equations - TEM/TE/TM/HE wave definitions.	10
Unit II	Waveguides Propagation of Waves and Rectangular waveguide – Propagation of TEM waves – TE and TM Modes- Propagation of TM waves and Rectangular waveguide – Propagation of TE waves and Rectangular waveguide – TE, TM modes in rectangular waveguide.	12
Unit III	Microwave Components, Instruments and Microwave Tubes Overview of Microwave Components, Measurement devices and instruments – Two Cavity Klystron Amplifier –Multicavity Klystron – Two Cavity Klystron Oscillator – Reflex Klystron – Travelling Wave Tube–Backward Wave Oscillator – Magnetrons.	14
Unit IV	Optical Fibers And Their Properties Introduction to Optical Fiber – Basic Structure of Optical Fiber – Total Internal Reflection – Principles of light propagation – Types of fibers: Step Index & Graded Index fibers. Modes of Propagation: Single and Multimode fibers – Acceptance Angle – Numerical Aperture – Advantage and Application.	12
Unit V	Light Sources & Photo Detectors Light Sources: LED - Fibre LED Coupling – LASERS. Photo Detectors: Characteristics – Photo Emissive Type – Photo Conductive – Photo Voltaic Devices – PIN Photo diode – Avalanche Photo Diode. Application: ADM – CATV – Digital Video Transmission.	12

- 1. M.Kulkarni, "Microwave and Radar Engineering", Umesh Publications, 2nd Edition, 2009.
- 2. Subir Kumar Sarkar, "Optical Fibers and Fiber Optic Communication Systems", S. Chand Publication, 2nd Edition 2001.

Reference Books:

- Annapurna Das & Sisir K. Das, "Microwave Engineering", Tata McGraw Hill Publications, 2nd Edition, 2009.
 Gerd Keiser, "Optical Fiber Communications", TMH, 4th Edition, 10th Reprint, 2011.
- 3. Samuel Y. Liao, "Microwave Devices and Circuits", Pearson Education, Third Edition, 2008.

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Code No.	Subject	Semester No.
17ELU13	INTEGRATED CIRCUITS AND INSTRUMENTATION	IV
Objective:	To learn the basics of IC fabrication techniques and to equip the students wit	
	knowledge of various analog IC's, Transducers and basic electronic instruments.	
Unit No	Topics	Hours
Unit I	IC Fabrication Technology Introduction – Fundamentals of Monolithic IC technology – Basic plan process – Wafer preparation – Epitaxial growth – Oxidation Photolithography – Diffusion of impurities – Isolation techniques Metallization – Monolithic transistors –Integrated resistors – Integrated capacitors – Integrated inductors – Thin and Thick Film Technology.	_ 12
Unit II	Operational Amplifier Op-Amp Non-Inverting and Inverting Amplifiers — Instrumentation Amplifier — V to I and I to V Converters — Differentiator and Integrator Sample and Hold Circuits — Log and Antilog Amplifiers — Comparators— Square Wave Generator — Monostable Multivibrator — Triangle Wave Generator — Saw-tooth Wave Generator — Phase Shift Oscillator — Wieler Bridge Oscillator .	12 ve
Unit III	Timer and PLL Block diagram of Timer (NE555) – Monostable Operation. Application of Monostable Mode – Astable Operation – Applications of Astable Mode – Basic Principle of PLL – Phase Detector – VCO – Low Par Filter – Monolithic PLL NE 565 – PLL Applications – Voltage Controlle Oscillator (LM566).	ole 12
Unit IV	Transducers Electrical Transducer – Advantages – Classifications – Characteristics at Choice of Transducer – Resistive Transducers – Potentiometers – Sta Gauge – Thermistors – Thermocouples – Variable Inductan Transducer – LVDT – RVDT – Capacitive Transducer – Piezoelectr Transducers – Hall Effect Transducers – Optoelectronic Transducers.	in ce 12
Unit V	Electronic Instruments Q Meters- CRO: Block Diagram – Cathode Ray Tube – Measurement Frequency – Measurement of Voltage and Current – Digit Oscilloscope– Digital Voltmeter: Ramp Type DVM – Dual Slog Integrating type DVM – Digital Multimeter – Humidity Measurement Measurement of PH.	tal 12

- 1. D. Roy Choudhury & Shail B. Jain, "Linear Integrated Circuits" New Age International, 2004. (Unit- I,II & III)
- 2. J.B.Gupta "A Course in Electronic and Electrical Measurements and Instrumentation", S.K Kataria & sons, 12th Edition. (Unit -IV & V)

Reference Books:

- 1. K.R.Botkar, "Integrated Circuits", Khannan Publishers, 1991.
- 2. Ramakant A. Gayakwad, "Op-Amps and LinearIntegrated Circuits", PHI, 2nd Edition, 1991.
- 3. H.S. Kalsi, "Electronic Instrumentation", Tata McGraw Hill Publication Company Limited, Sixth Reprint, 2006.

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Code No.	Subject	Semester No.
17ELU14	PCB DESIGN AND FABRICATION	IV
Objective:	To enable the students to learn the design concept of PCB and become PCB fabrication techniques.	e familiarize in
Unit No	Topics	Hours
Unit I	Types of PCB Single sided board – double sided – Multilayer boards – Plated throug holes technology – Benefits of Surface Mount Technology (SMT) Limitation of SMT – Surface mount components: Resistors, Capacito Inductor, Diode and IC's.	_ 12
Unit II	Layout and Artwork Layout Planning – General rules of Layout – Resistance, Capacitance an Inductance – Conductor Spacing – Supply and Ground Conductors Component Placing and mounting – Cooling requirement and packaged density – Layout check. Basic artwork approaches – Artwork tapinguidelines – General artwork rules – Artwork check and Inspection.	_ 12
Unit III	Laminates and Photo Printing Manufacture of copper clad laminates – Properties of laminates – Type of Laminates – Manual cleaning process – Basic printing process for double sided PCB's – Photo resists – wet film resists – Coating process for wet film resists – Exposure and further process for wet film resists – Dry film resists	r 12
Unit IV	Etching and Soldering Introduction – Etching machine – Etchant system. Soldering: Principles of Solder connection – Solder joints – Solder alloys – Soldering fluxes. Soldering Tools: Soldering, Desoldering tools and Techniques – Ma Soldering – Solder mask – Safety, health and medical aspects in Soldering practice.	12
Unit V	Design Rules and Automation Reflection – Crosstalk – Ground and Supply line noise – Electromagneti interference from pulse type EM fields and automation – Automate artwork drafting – CAD.	2 d

1. Walter C.Bosshart, "PCB Design and Technology", Tata McGraw Hill Publications, Delhi. 2008.

Reference Books:

- Clyde F.Coombs "Printed Circuits Handbook" McGrawhill, 7th Edition, 2016.
 R.G.Gupta "Electronic instruments and system" Tata McGraw Hill Publication. NewDelhi, 2001.
- 3. Christopher T. Robertson, "Printed Circuit Board Designer's Reference: Basics", Prentice Hall, 2004.

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Subject	Semester No.
PRACTICAL IV: ELECTRONIC CIRCUITS LAB	IV

(Any 18 Experiments)

- 1. Half Wave Rectifier.
- 2. Full Wave Rectifier.
- 3. Filter Circuits.
- 4. DC Power Supply Design using IC 78XX and 79XX Series.
- 5. Clipper Circuit.
- 6. Clamper Circuit.
- 7. Voltage doubler.
- 8. Transistor Biasing Circuit.
- 9. RC Coupled Amplifier.
- 10. Transformer Coupled Amplifier.
- 11. Emitter Follower.
- 12. Class B Power Amplifier.
- 13. Class AB Power Amplifier.
- 14. Feedback Amplifier.
- 15. RC Phase Shift Oscillator using BJT.
- 16. RC Phase Shift Oscillator using FET.
- 17. Wien Bridge Oscillator using BJT.
- 18. Hartley Oscillator using BJT.
- 19. Colpitt's Oscillator using BJT.
- 20. Clapp Oscillator using BJT.
- 21. Crystal Oscillator.
- 22. Monostable Multivibrator using BJT.
- 23. Astable Multivibrator using BJT.
- 24. Bistable Multivibrator using BJT.
- 25. Schmitt Trigger using BJT.

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IV

(Any 18 Experiments)

- 1. Verification of Basic Logic Gates and Universal Gates.
- 2. Verification of Boolean Laws and Demorgan's Theorem.
- 3. Parity Generator and Checker.
- 4. Half Adder and Full Adder.
- 5. Half Subtractor and Full Subtractor.
- 6. Multiplexer and De multiplexers.
- 7. Encoder and Decoder.
- 8. BCD to 7-Segment Display.
- 9. Flip Flops.
- 10. Shift Registers and Ring Counter.
- 11. Analog to Digital Converter.
- 12. Digital to Analog Converter.
- 13. Inverting and Non-inverting Amplifier using Op-Amp.
- 14. Adder and Subtractor using Op-Amp.
- 15. Instrumentational Amplifier using Op-Amp.
- 16. First and Second order Low pass filter using Op-Amp.
- 17. First and Second order High pass filter using Op-Amp.
- 18. Band pass filter using Op-Amp.
- 19. Notch filter using Op-Amp.
- 20. Phase Shift and Wein Bridge Oscillator using Op-Amp.
- 21. Square Wave Generator using Op-Amp.
- 22. Zero crossing detector and Schmitt Trigger using Op-Amp.
- 23. Monostable Mutivibrator using Timer.
- 24. Astable Mutivibrator using Timer.
- 25. Voltage Controlled Oscillator using NE566.

Code No.	Subject	Semester No.
17ELU17	PRACTICAL VI: COMMUNICATION SYSTEMS LAB	IV

(Any 10 Experiments)

- 1. Amplitude Modulation and Demodulation.
- 2. Frequency Modulation LM565/NE565.
- 3. PAM Modulation and Demodulation.
- 4. PWM Modulation and Demodulation.
- 5. PPM Modulation and Demodulation.
- 6. Voltage Controlled Oscillator using Timer.
- 7. ASK Generation and Detection.
- 8. FSK Generation and Detection.
- 9. PSK Generation and Detection.
- 10. BPSK & DPSK Generation and Detection.
- 11. QAM Generation and Detection.
- 12. Pulse Code Modulation and Demodulation.
- 13. Delta & Adaptive Delta Modulation and Demodulation.
- 14. Establishment of Analog Fiber Optic Link.
- 15. Establishment of Digital Fiber Optic Link.

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Code No.	Subject	Semester No.
17ELU18	CELLULAR COMMUNICATION SYSTEM	V
Objective:	To enrich the basics of Cellular Communication Techniques to the Students.	
Unit No	Topics	Hours
Unit I	Cellular Mobile System Introduction – Basic Cellular System – Performance criteria – Operation of Cellular System – Analog Cellular System – Digital Cellular System Elements of Cellular Mobile System Design: Frequency reuse channels Co-channel interference reduction factor – Handoff Mechanism – Cellular Splitting.	- - 13
Unit II	Cell Coverage for Signal and Traffic Mobile Point-to-point Model – Propagation over Water – Foliage loss Propagation in Near-in Distance – Long distance propagation – Cell signatenna heights and signal coverage cells – Mobile-to-Mobile propagation.	te 13
Unit III	Frequency Management and Channel Assignment Frequency Management – Frequency Spectrum Utilization – Set-uchannels – Channel Assignment – Fixed Channel Assignment – No Fixed Channel Assignment – Operate with additional Spectrum – Traff and Channel Assignment – Perception of Call blocking from the Subscribers.	in 10
Unit IV	Handoffs and Dropped Calls Implementing Handoffs – Initiation of a Handoff – Delaying a Handoff Forced Handoffs – Queuing of Handoffs – Power difference Handoffs Mobile assisted Handoff and Soft Handoff – Cell-site Handoff Intersystem Handoff – Dropped call rate – Formula of Dropped carate– Finding the values of δ and μ .	_ 12
Unit V	Digital Cellular Systems Global System for Mobile (GSM) – GSM Architecture – Laye modeling – Transmission – Radio resource management – Mobilit Management – Communication Management – TDMA Architecture Transmission and Modulation – CDMA – Terms of CDMA systems Call processing – Hand over Procedures.	У 12

1. William C.Y.Lee, "Mobile Cellular Telecommunications", McGraw Hill Publications, Second Edition, 2008.

Reference Books:

- 1. Gottapu Sasibhushana Rao, "Mobile Cellular Communication", Pearson, 2013.
- V.Jeyasri Arokiamary, "Cellular and Mobile Communications", Technical Publications Pune, 1st Edition, 2009.
- 3. William C.Y.Lee, "Wireless and Cellular Telecommunications", Tata Mc-Graw Hill, 3rd Edition, 2006.

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Code No.	Subject	Semester No.
17ELU19	VLSI DESIGN TOOLS	\mathbf{V}
Objective:	To import the concepts of Digital Circuit Design using VHDL and equip	the Students
	to Develop New Digital Systems.	
Unit No	Topics	Hours
Unit I	Introduction and Basic Concepts of VHDL History of VHDL - Capabilities of VHDL - Hardware Abstraction Basic Terminology - Entity Declaration - Architecture Boo Declaration- Basic Language Elements - Identifiers - Data Objects- Data Types and Operators.	dy 12
Unit II	Behavioral Modeling Techniques of VHDL Behavioral Modeling: Entity Declaration – Architecture Declaration Process Statements – Variable Assignment Statements – Sign Assignments Statements – Wait Statement – IF Statement – Ca Statement – Null Statement – Loop Statement – Exit Statement – Ne Statement – Assertion Statement – Report Statements – More on Sign Assignment Statement – Multiple Process – Postponed Process.	se 12
Unit III	Data Flow Structural Modeling Techniques of VHDL Data Flow Style of Modeling: Concurrent Signal Assignment Statemen Versus Signal Assignment Statement – Delta Delay Revisited – Multip Drivers – Conditional Signal Assignment Statement – Selected Signal Assignment Statement – Unaffected Value – Block Statement Concurrent Assertion Statement.	ole 12
Unit IV	Structural Modeling Techniques, Generics & Configuration VHDL Structural Modeling: Component Declaration – Component Instantiation Resolving Signal Value – Examples of Structural Modeling– Half Adde Full Adder – Four to One Multiplexers – Decoders and Encoder Generics – Configuration – Configuration Specification – Configuration Declaration – Default Rules – Conversion Functions – Directors – Incremental Binding	n- r- rs. on
Unit V	Advanced Features in VHDL Subprograms - Sub Program Overloading - Operator Overloading Signatures - Default Value of Parameters - Package Declaration Package Body - Design File - Design Libraries - Implicit Visibility Explicit Visibility.	- 12

1. J. Bhaskar, "VHDL Primer", Low price Edition, PHI, 2001.

- 1. Charles H. Roth & Jr. Lizy Kurian John, "Digital System Design Using VHDL" Cengage Learning.
- Douglas L. Perry, "VHDL: Programming by Example", Tata McGraw Hill, 4th Edition, 2002.
 Kenneth L. Short, "VHDL for Engineers", Pearson, 1st Edition, 2009.



Code No.	Subject	emester No.
17ELU20	8051 MICROCONTROLLER	V
Objective:	To enable the students to learn the instruction set, programming, and interfacencepts of 8051 microcontroller.	cing
Unit No	Topics	Hours
Unit I	Overview and Instruction Set Introduction to Computing – Microprocessor and Microcontrollers – Microcontrollers and Embedded Processors – Overview of 8051 Family– 8051 Architecture – Timers – Registers and Memory Organizations.	1.20
Unit II	8051 Assembly Language Programming Inside the 8051 – Pin Out – Instruction Set: Addressing Modes – Data Transfer Instruction – Logical Instruction – Arithmetic Instructions – Jump and Call Instructions – Bit Oriented Instructions – Flags and Stack.	12
Unit III	Programming with C Data types – Time delay programming – I/O programming – Logic Operations – Arithmetic Operations – Timer Programming – Counter Programming.	1. 60
Unit IV	8051 Interrupts & Peripherals 8051 Interrupts - Programming External Hardware Interrupts - 8051 Serial Communication Programming - Programming with Serial Communication Interrupts - Peripheral and Interrupt Programming in C.	12
Unit V	Real World Applications and Case Studies LCD Interfacing – Keyboard Interfacing – Parallel and Serial ADC Interfacing – DAC Interfacing – Sensor Interfacing and Signal Conditioning – RTC Interfacing – Relays and Opto-Isolator Interfacing – Stepper Motor Interfacing – DC Motor Interfacing and PWM.	1.0

Muhammad Ali Mazidi, Janice Gillispie Mazidi and Rolin D. McKinlay, "The 8051 Microcontroller and Embedded Systems using Assembly and C", PHI, 2nd Edition, 2006.

- Kenneth J. Ayala, "The 8051 Microcontroller", Delmar Cengage Learning, 3rd Edition. D. Karuna Sagar, "Microcontroller 8051", Narosa Publishing House, 2011.
- 3. A.P. Godse, D.A. Godse, "Microprocessor and Microcontroller", Technical Publications Pune, First Edition, 2007.

Code No.	Subject	Semester No.
17ELU21	PRACTICAL VII: CIRCUIT SIMULATION LAB	V

(Any 10 Experiments)

(Using PSpice/MULTISIM/MATLAB/Proteus/Lab VIEW/Equivalent Simulation Software)

- 1. Voltage and Current Divider
- 2. Super position Theorem
- 3. Thevenin's Theorem
- 4. Norton's Theorem
- 5. Maximum Power Transfer Theorem
- 6. Half Wave and Full Wave Rectifier
- 7. Low Pass and High Pass Filter
- 8. Band Pass and Band Reject Filter
- 9. Clipper and Clamper
- 10. RC Coupled Amplifier
- 11. RC Phase Oscillator
- 12. Wien Bridge Oscillator
- 13. Adder and Subtractor using OPAMP
- 14. Schmitt Trigger using OPAMP
- 15. Astable and Monostable Multivibrator

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Code No.	Subject	Semester No.
17ELU22	ELECTIVE – I: (A)	V
	MEDICAL ELECTRONICS	
Objective:	To enable the students to learn about the concepts of bio-potentials and bio-	
	instruments and also to develop the troubleshooting skills of bio-medical instruments.	
Unit No	Topics	Hours
Unit I	Human Physiological Systems Cells and their structure – Transport of potentials – Bio-electric potential Bio-potential Electrodes: Design of medical instruments – Componen of Bio medical instrument system – Electrodes – Half cell potential purpose of the electrode paste. Types of electrode: Microelectrode Depth and Needle electrodes – Surface electrode	ts 9
Unit II	Bio Potential Recorders Characteristics of the recording system – Electrocardiography – Origin of cardiac action potential – ECG lead configurations – ECG recording setup. Practical considerations for ECG recording – Echocardiograph Electroencephalography (EEG): Origin of EEG – Brain waves Placement of electrodes – Recording setup.	ıg 9 y.
Unit III	Diagnostic Equipments Electromyography (EMG): Recording setup – Determination of conduction velocities in motor nerves— Electroretinography (ERG) Electro Oculography (EOG) – Blood pressure measurement (Indire methods) – Audio meter – X- ray machine.	- 9
Unit IV	Biotelemetry X-ray imaging – Radio fluoroscopy – Image Intensifiers – Angiography Endoscopy – Diathermy. Biotelemetry and Patient Safety: Need for biotelemetry – Elements of telemetry system – Radio telemetry system Physiological signals used in telemetry – TDM and FDM – Implantab units.	or 9
Unit V	Physiological Assist Devices Need for Pacemakers – Pacemaker parameters and circuits – Difference modes of operation – DC defibrillator – Artificial heart valves – Healung machines – Artificial lung machines – Artificial kidney machine Nerve and Muscle stimulator. Computer Applications: Data acquisition systems – Analysis of ECG signals – Computerized Axial Tomograph (CAT) Scanner – Ultrasonic scanner – Magnetic resonance imaging Computer based patient monitoring system.	rt - 9

- 1. Arumugam.M, "Biomedical Instrumentation", Anuradha Agencies Publishers, Chennai, 1992.
- 2. Khandpur, "Handbook on Biomedical Instrumentation", Tata McGraw Hill Company, New Delhi, 1989.

Reference Books:

- 1. Joseph J. Carr and John M. Brown, "Introduction to Biomedical Equipment Technology", Pearson Education Asia, New Delhi, 4th Education, 2001.
- 2. Leslie Cromwell., Fred J. Weibell., Erich A. Pfeiffer., "Bio-medical Instrumentation and Measurements", Prentice Hall of India, New Delhi, 1990.
- 3. John G Webster, Ed., "Medical Instrumentation Application and Design", Third edition, John Wiley & Sons, Singapore, 1999

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Code No.	Subject	Semester No
17ELU22	ELECTIVE – I: (B)	V
	CONSUMER ELECTRONICS	
Objective:	To enable the students to learn about the idea implementation of electronics is	
TT 1. NT	consumer products.	
Unit No	Topics	Hours
Unit I	Car Computers Electronic Ignition – Electronic Ignition Lock System – Antilock Brakin System (ABS) – Electronically controlled suspension (ECS) – Instrument panel displays – Ultrasonic car safety belt system – Air bag system Vehicle proximity detection systems – car navigation systems.	it 9
Unit II	Microwave Ovens Microwaves – Properties and generation – Microwave oven block diagram – LCD timer with alarm – Controllers Types of Microwave ovens – Microwave Cooking Safety instructions – Care and Cleaning Washing Machines: Electronic controller for washing machines – Washing machine hardware and software – Types of washing machines Fuzzy logic washing machines – Features of washing machines.	9
Unit III	Air Conditioners Air Conditioning: Components of air conditioning systems – All water air conditioning systems – All air conditioning systems – Unitary and centra air conditioning systems – Split air conditioners. Refrigerators Refrigeration – Refrigerants – Refrigeration systems – Domestic refrigerators.	9
Unit IV	Home / Office Digital Devices Facsimile machine – Xerographic copier Calculators – Structure of a calculator – Internal Organization of a calculators – Servicing electronic calculators – Digital clocks – Block diagram of a digital clock.	9
Unit V	Digital Access Devices Bar codes – Bar coding – Bar code scanner and decoder – Automated Teller Machines (ATM): Electronic Funds Transfer – Point of sale Terminal – Automated Teller Machines Set-Top Boxes: Interoperable set-top boxes – Middle ware for set-top boxes – Personal Video Recorder – Digital cable TV – Video on demand.	9

Text Book.

1. S.P. Bali, "Consumer Electronics", Pearson Education, New Delhi, 2005.

Reference Books:

- 1. J.S.Chitode, "Consumer Electronics", Technical Publications, 2007.
- 2. Douglas Kinney, "A Beginners Guide to Consumer Electronics Repair: Hand Book and Tutorial", iUniverse, 2006.
- 3. Philip Hoff, "Consumer Electronics for Engineers", Cambridge University Press, 1998.

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Code No.	Subject	Semester No.
17ELU23	SATELLITE AND NETWORK COMMUNICATION	VI
Objective:	To enable the students to learn & enhance the knowledge in satellit communication system.	e and network
Unit No	Topics	Hours
Unit I	Communication Satellite Orbit and Description History of Satellite Communication – Satellite Frequency Bands Satellite Systems – Applications – Orbital Period and Velocity – Effect of Orbital Inclination – Azimuth and Elevation – Coverage angle ar slant Range – Eclipse – Orbital Perturbations – Placing Satellites into Geo-Stationary orbit.	ts ad 12
Unit II	Satellite Sub-Systems Attitude and Orbit Control system – TTC&M Subsystem – Attitude Control Subsystem – Power Systems – Communication subsystems Satellite Antenna Equipment. Satellite Link: Basic Transmission Theory System Noise Temperature and G/T ratio – Basic Link Analysis Interference Analysis – Design of satellite Links for a specified C/N (With and without frequency Re-use) – Link Budget.	_ _ _
Unit III	Data Communication Introduction – Basic Terms and Concepts – Line Configurations Topology – Transmission Media – MODEM: Standard and Types Analog and Digital transmission: Encoding and Modulating – Channe Capacity – Base Band and Broad Band – Transmission Impairments Multiplexing: FDM – TDM – Error Detection and Control: CRC.	- 12 el
Unit IV	Network Architecture and Protocols Layered Architecture – OSI model – Functions of Layers – Data Lin Control Protocols – ARQ – Stop and Wait – Sliding Window – Go bac N and Selective Repeat – Asynchronous Protocol: X Modem – Modem– Kermit – Synchronous Protocol: BSC– SDLC – HDLC– TCP/I Model – SMTP – HTTP – FTP.	12 Y
Unit V	LAN and ISDN LAN: Standard, Protocol – IEEE 802 Standards: ETHERNET – LLC MAC – CSMA/CD – Token Ring – Token Bus – FDDI –ALOHA SONET – ISDN: IDN – Channels – User Interfaces – ISDN Layers Broad Band ISDN – Frame Relay – ATM: Concept and Architecture ISDN Protocol– Physical Layer Protocol – D-channel Data Link Layer Layer 3 Protocols– Network Signaling Systems: SS7 Protocol.	12

- Timothy Pratt, Charles Bostian & Jeremy Allnutt, "Satellite Communications" John Wiley & Sons, 2nd Edition, 2003. (Unit - I&II)
- Behrous. A. Forouzan, "Data Communication and Networking", Tata McGraw Hill, 4th Edition, 2000. (Unit – III, IV and V)

- Satellite Communications-Dennis Roddy, McGraw Hill, 2nd Edition, 1996.
 Ulysess Black, "Data Communications and Distributed Networks", 3nd Edition, 2012.
- 3. Ray E.Sheriff & Y.Fun Hu, "Mobile Satellite Communication Network", Wiley-India Edition, 2006.



Code No.	Subject	Semester No.
17ELU24	INDUSTRIAL AND POWER ELECTRONICS	VI
Objective:	To enable the students to learn and design the industrial & power electron	nic circuits
	and also to impart knowledge of industrial electronics system design.	
Unit No	Topics	Hours
Unit I	Overview of Thyristor Introduction - Symbolic representation - Specifications - Thyristo ratings - Construction - Operating Principle of an SCR - Two transisto analogy of SCR - Comparison of Thyristors with Gas Tubes and Transistors - Construction and Operating principle of DIAC, TRIAC UJT, SCS, SUS, SBS, LASCR and LASCS.	12
Unit II	Thyristor Triggering and Commutation Methods of triggering a Thyristor: Thermal Triggering – Radiation Triggering – Voltage triggering – dv/dt triggering – Gate triggering Commutation of a Thyristor: Natural Commutation – Forced Commutation – Thyristor Configurations.	. 12
Unit III	Inverters and Choppers Inverters: Working Principle – Thyristor based inverters – Series & Parallel inverters – Bridge inverters – Mc-Murray Bedford inverter – Voltage control and Current Source inverters. Choppers: DC Chopper – Single-Thyristor Chopper, Two-Thyristor Chopper, Morgan Chopper Circuit, Step-up Chopper – AC Chopper.	12
Unit IV	Dual Converters and Cycloconverters Dual Converters: Block diagram— Types: Single-phase dual converter— Three-phase dual converter— Non-circulatory current dual converter— circulating current dual converter. Cycloconverters: Block diagram— Types: Single-phase Cycloconverter— Three-phase Cycloconverter.	12
Unit V	Applications of Thyristors Temperature control – Illumination Control – TRIAC as a Three-position static switch – Automatic Street Lighting Circuit using LDR and SCR – Emergency Light using SCR – Automatic Water Level Indicator using SCR – Automatic Battery Charger using SCR – Light operated SCR Alarm – Burglar Alarm Circuit using SCR – Flip-Flop Circuit using SCR.	12

1. S.K.Bhattacharya and S. Chatterjee, "Industrial Electronics and Control", Tata McGraw-Hill Publishing Company Limited, 13th Reprint, 2006.

Reference:

- 1. Rashid M.H, "Power Electronics Circuits, Devices and Applications", Prentice Hall, Third Edition, 2011.
- 2. Dr.Bimbhra.P.S. "Power Electronics", Khanna Publishers, Fifth Edition, 2014.
- 3. http://nptel.ac.in/courses/108101038.

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Code No.	Subject	Semester No
17ELU25	EMBEDDED SYSTEMS	VI
Objective:	To enable the students to learn the idea behind the Embedded System.	
Unit No	Topics	Hours
Unit I	Introduction to Embedded Systems Embedded Systems – Processor embedded into the System –Hardware units – Embedded Software in a system – Exemplary Embedded Systems– Embedded SOC and use of VLSI circuit technology – Complex System Designs and Processors – Design Process in Embedded System – Classification of Embedded Systems.	12
Unit II	PIC 16F877 Architecture and Instruction Set Device Overview – Architecture – Memory Organization – Status Register – Option Register – INTCON Register – PCON Register – I/O Ports – Data EEPROM – Instruction Set: Byte Oriented Operations – Bit Oriented Operations – Literal and Control Operations.	12
Unit III	External Interrupts and Timers RB0/INT External interrupt - Timer0 - Compare Mode - Capture Mode- Timer1 External Event Counter - PWM module - Port B - Change interrupts.	12
Unit IV	I/O Ports and Serial Port Interface I/O Ports – Synchronous serial Port Module – Serial Peripheral Interface – I2C Bus Interface – ADC Converter – USART.	12
Unit V	Special Features Configuration Word- Oscillator Configurations – Reset Alternatives – Low power Operation- Low Voltage Serial Programming – Parallel Slave Port.	12

- 1. Raj Kamal, "Embedded Systems Architecture, Programming and Design", TMH, 2007.
- 2. John B. Peatman, "Design with PIC Micro Controllers", Pearson Education, 2009.

Reference:

- 1. PIC 16F87X Data book, Microchip Technology Inc, 2001.
- 2. Muhammad Ali Mazidi, Rolind D. McKinlay and Danny Causey, "PIC Microcontroller and Embedded Systems using Assembly and C for PIC18", Pearson Education, 2013.
- 3. http://nptel.ac.in/courses/117104072/.

Code No.	Subject	Semester No.
17ELU26	PRACTICAL VIII: INDUSTRIAL ELECTRONICS LAB	VI

(Any 18 Experiments)

- 1. Resistance Triggering of SCR.
- 2. RC Phase Shift Triggering of SCR.
- 3. UJT Triggering of SCR.
- 4. Half Controlled Bridge Rectifier.
- 5. Power Control using SCR.
- 6. Automatic Street Light Control using DIAC and TRIAC.
- 7. Phase Control using TRIAC.
- 8. AC Motor Control using SCR.
- 9. Design of Snubber Circuit.
- 10. Fan Regulator using TRIAC.
- 11. Thyristor Chopper.
- 12. Burglar Alarm.
- 13. Switching Regulators.
- 14. ON / OFF Relay Control using Opto coupler.
- 15. Temperature controller using AD 590/LM 35.
- 16. Digital Bidirectional control using TRIAC.
- 17. Servo Stabilizer.
- 18. Cycloconverter.
- 19. Thyristor protection circuit.
- 20. Light Dimmer.
- 21. Automatic Battery Charger.
- 22. Fire Alarm.
- 23. Power Inverter.
- 24. Time delay circuit.
- 25. DC Motor Controller.

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Code No.	Subject	Semester No.
17ELU27	PRACTICAL IX: EMBEDDED SYSTEMS LAB	VI

(Any 18 Experiments)

(Using 8051 / PIC16F877 / PIC16F84A)

- 1. 16-bit Addition.
- 2. 8-bit Array Addition.
- 3. Subtraction of 8 -bit and 16-bit Numbers.
- 4. Square root of array of number.
- 5. Factorial of array of number.
- 6. Sort an array in an Ascending and descending order.
- 7. Largest/Smallest number among the array of data.
- 8. Generate the Fibonacci series.
- 9. Find odd/ even number.
- 10. Arithmetic and Logic Operations.
- 11. Data transfer with parallel port.
- 12. Time Delay generation using internal timer.
- 13. PWM Generation.
- 14. Object Counter.
- 15. Interfacing Matrix Keypad.
- 16. LCD Interface.
- 17. ADC Interface.
- 18. DAC Interface.
- 19. Solid State Relay Interface.
- 20. Seven Segment Display Interface.
- 21. Traffic Light Controller.
- 22. Water level controller.
- 23. Stepper Motor Interface.
- 24. Serial Communication.
- 25. Digital Clock.

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Code No.	Subject	Semester No.
17ELU28	ELECTIVE – II: (A)	VI
Ohiostiva	ROBOTICS AND AUTOMATION	
Objective:	To introduce the basic concepts, parts & types of robots and to make the studen familiar with sensors & their applications in robots and programming of robots.	
Unit No	Topics	
011110	South A Street	Hours
Unit I	Introduction to Robotics History – Laws of robotics – Robot definition – Robot usage rules Applications – Robot subsystems: Manipulators – End effectors Actuators – Transmissions – Sensors and controllers – Classification of robots: Cartesian robot – Cylindrical robot – Spherical robot – Articulate robot.	- 12 of
Unit II	Sensors in Robotics Requirements of a sensor – Tactile sensors – Temperature sensors LVDT Sensor – High Resolution Pneumatic tactile Sensor – Slip typ Sensors – Piezo electric Contact Sensors – Remote Sensor – Compliance Range and Proximity Sensors – Electro-optical Sensors – CCD Camera.	e 12
Unit III	Actuators Electrical actuator systems: Solenoids – Relays – Solid state switches DC Motors – AC motors – Servo motor and stepper motor – Vacuur grippers – Mechanical gripper – Magnetic gripper – Mechanical actuato systems: Involving linkages – Gears – Ratchet and pawl – Belt and chaidrives – Bearings.	m 12
Unit IV	Electronics for robot Introduction to Microcontrollers – The Arduino Platform – Arduin Board– Arduino Family – Fundamentals of Arduino Programming Keywords – Inbuilt functions – Libraries – Digital GPIO programming Working with pins as input and output – Working with PWM Outputs Working with Analog Inputs using on-chip ADC – Serial Communicatio between Arduino hardware and PC – Arduino Interrupt Programming.	12
Unit V	Robot Applications Blinking of LED – LED brightness control using PWM – Motor Directio Control – Motor Speed Control using PWM – Name display in LCD – Il sensor Interface – Ultrasonic sensors interface – Tone generation – Lin follower Robot – Obstacle avoider Robot – Never Falling Robot Wireless Robot – PC control Robot.	R 12

- 1. Saha, S.K., "Introduction to Robotics", 2nd Edition, McGraw-Hill Education, New Delhi, 2014.
- 2. S.R. Deb, "Robotics Technology and Flexible Automation", 2nd edition, Tata McGraw Hill, New Delhi.
- 3. Michael McRoberts, "Beginning Arduino", 2nd edition, Apress, 2013.
- 4. John-David Warren, Josh Adams, Harald Molle, "Arduino Robotics", Apress, 2011.

Reference:

- 1. Maja J. Mataric, "The Robotics Primer", MIT Press, 2007.
- 2. B. R.Mittle, I.Nagrath, "Robotics and Control", Tata Mcgraw-Hill Education, 2003.
- 3. http://www.nptel.ac.in/courses/112102011/11.
- 4. http://nptel.ac.in/courses/112101099/.
- 5. D. http://nptel.ac.in/courses/112108093/.

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Code No.	Subject	Semester No.
17ELU28	ELECTIVE – II: (B)	VI
011	AUTOMOTIVE ELECTRONICS	
Objective:	To enable the students to get knowledge in automotive components, operating & working principle.	
Unit No	Topics	Hours
Unit I	Introduction System approach – Electrical Wiring, Terminals and Switching Multiplexed Wiring Systems – Circuit Diagrams and Symbols. Chargin Systems and Starting Systems: Charging System Principles – Alternator and Charging Circuits – New Developments. Requirements of the Startin System – Starter motor and Circuits.	g 12
Unit II	Ignition Systems Ignition fundamentals – Electronic Ignition – Programmed Ignition Distributorless Ignition – Direct Ignition – Spark Plugs. Electronic Fuc Control: Basics of Combustion – Engine Fuelling and Exhaus Emissions– Electronic Control of Carburation – Petrol Fuel Injection Diesel Fuel Injection.	el 12
Unit III	Instrumentation Systems Introduction to Instrumentation Systems – Various Sensors used for Different Parameters – Sensing Driver Instrumentation Systems – Vehicle Condition Monitoring Trip Computer – Different Types of Visual Display.	e 12
Unit IV	Electronic Control of Braking and Traction Description Control Elements and Control Methodology – Electronic Control of Automatic Transmission: Introduction and Description Control of Gear Shift and Torque Converter Lockup – Electric Power Steering – Electronic Clutch.	1 12
Unit V	Engine Management Systems Combined Ignition and Fuel Management Systems – Exhaust Emission Control – Digital Control Techniques – Complete Vehicle Control Systems – Artificial Intelligence and Engine Management – Automotive Microprocessor uses. Lighting and Security Systems: Vehicles Lighting Circuits – Signaling Circuit – Central Locking and Electric Window Security Systems – Airbags and Seat Belt tensioners – Miscellaneous Safety and Comfort Systems.	12

1. Tom Denton, "Automobile Electrical and Electronic Systems", Edward Arnold pb., 1995.

Reference Books:

- 1. Don Knowles, "Automotive Electronic and Computer controlled Ignition Systems", Don Knowles, Prentice Hall, Englewood Cliffs, New Jersey 1988.
- 2. William, T.M., "Automotive Mechanics", McGraw Hill Book Co.,
- 3. William, T.M., "Automotive Electronic Systems", Heiemann Ltd., London, 1978.
- 4. Ronald K Jurgen, "Automotive Electronics Handbook", McGraw Hill, Inc, 1999.

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